

# PADS® XE "D" Suite

## PCB Design and Layout, High Speed Rules and Analysis

PADS XE "D" Suite offers superior value for a complete PCB design system with pre-layout analysis, high speed rules and reuse capabilities.

### DxDesigner

- Robust design definition
- Intuitive project and design navigation
- Unlimited, hierarchical sheets
- Schematic design reuse
- Integrated physical decal viewer
- Design attribute and rules management
- Cross-probing with layout
- Full forward/back annotation with layout
- Symbol wizard

### HyperLynx® - LineSim

- **Pre-layout signal integrity analysis**

### PADS Layout / PADS Routers

- PCB editor
- Online design rule checking
- **Unlimited layer interactive routing**
- **4 Layer autorouting**
- **Unlimited database connections**
- **Design variants creation**
- **Physical design reuse**
- Library module
- Auto-dimensioning
- Split-plane tool
- Mechanical tool DXF link

### ARS Suite \*

- **Advanced rule setting including differential pairs**

*\*customer choice*

### DxSim \*

- **Analog simulation**

### Highlights

**Bold** items not included in PE Suite

**DxDesigner.** Integrated schematic capture, project and design navigation, unlimited hierarchical sheets, design reuse, symbol wizard, and physical decal viewer. PADS Layout and LineSim integration.

**DxSim.** Extends DxDesigner environment to include analog simulation, with one schematic driving simulation and PCB. Includes automatic drag 'n drop symbol generation from SPICE models, MonteCarlo analysis, and easy-to-use intelligent views of testbenches, model libraries, and waveform simulation data.

**HyperLynx SI Analysis.** Checks signal integrity, termination, and routing. Reviews clock topologies, board stackup. Analyzes trace lengths/effects.

**PCB Editor w/ARS.** Includes layout editor, advanced rule setting for layer, class, group, pin pair rules, and conditional levels and differential pairs; online design rule checking; auto-dimensioning, RF design support, split-plane tools, a DXF link, and unlimited database connections. Design for fabrication lets you check for acid traps and starved thermals, checks, silkscreen over pads and other fabrication defects.

**RF Design.** RF design support of import of complex copper shapes, coplanar/channel wave guide shielding, square and chamfered corners and area shielding with vias.

**PADS Interactive Router.** Fast interactive route editor with unique aids for the interactive routing of length-constrained signals.

**PADS AutoRouter 4L.** Shape-based autorouter with true diagonal, and any-angle routing, component fan out, pattern routing, and via optimization. Routes four simultaneous layers.

**Physical Design Reuse.** Saves design costs and time by enabling replication of "golden circuits" in new designs or multiple replications of circuits in a design.

**Assembly Variants.** Provides easy creation of design variants from a master design. Graphic previews depict variance drawings before running CAM plots.

