

# PADS® SE "D" Suite

## PCB Design and Layout, High Speed Rules, Interactive Routing, and Analysis

PADS SE "D" Suite offers superior value for a complete PCB design system with design testing and high speed interactive routing capabilities.

### DxDesigner

- Robust design definition
- Intuitive project and design navigation
- Unlimited, hierarchical sheets
- Schematic design reuse
- Integrated physical decal viewer
- Design attribute and rules management
- Cross-probing with layout solutions
- Full forward/back annotation with layout
- Symbol wizard

### HyperLynx® - LineSim

- Pre-layout signal integrity analysis

### DxSim

- Analog simulation

**Bold** items not included in PE, XE Suites

### PADS Layout / PADS Routers

- PCB layout editor
- **High speed interactive routing**
- **8 Layer autorouting**
- **Design for test**
- Advanced rules including differential pairs
- Design variants creation
- Physical design reuse
- Auto-dimensioning
- Split-plane tool
- Library module
- Online design rule checking
- Unlimited database connections

### Highlights

**DxDesigner.** Integrated schematic capture, project and design navigation, unlimited hierarchical sheets, design reuse, symbol wizard, and physical decal viewer. PADS Layout and LineSim integration.

**DxSim.** Extends DxDesigner environment to include analog simulation, with one schematic driving simulation and PCB. Includes automatic drag 'n drop symbol generation from SPICE models, MonteCarlo analysis, and easy-to-use intelligent views of testbenches, model libraries, and waveform simulation data.

**HyperLynx SI Analysis.** Checks signal integrity, termination, and routing. Reviews clock topologies, board stackup. Analyzes trace lengths/effects

**PCB Editor w/ARS.** Includes layout editor, advanced rule setting for layer, class, group, pin pair rules, and conditional levels and differential pairs; online design rule checking; auto-dimensioning, RF support, split-plane tools, a DXF link, and unlimited database connections. Design for fabrication lets you check for acid traps and starved thermals, checks, silkscreen over pads and other fabrication defects.

**RF Design.** RF design support of import of complex copper shapes, coplanar/channel wave guide shielding, square and chamfered corners and area shielding with vias.

**PADS High Speed Interactive Router.** Critically acclaimed interactive route editor with unique aids for semi-automatic routing of design-rules driven signals.

**PADS AutoRouter 8L.** Shape-based autorouter with true diagonal and any-angle routing. Includes component fan out, pattern routing, and via optimization. Enables simultaneous routing of 8 layers.

**Physical Design Reuse.** Saves design costs and time by enabling replication of "golden circuits" in new designs or multiple replications of circuits in a design.

**Assembly Variants.** Provides easy creation of design variants from a master design. Graphic previews depict variance drawings before running CAM plots.

**Design for Test.** Perform testability auditing during the PCB design using embedded auditing tools developed for programming in-circuit test equipment. DFT Audit provides automated database updates for test point insertion, test point preservation features and enables the test engineer to evaluate testability of PCB's based on user defined test strategies for optimal results. Also includes PADS AutoRouter testpoint autorouting capability to optimize testpoint insertion.

